

Fig. 1A (Prior Art)

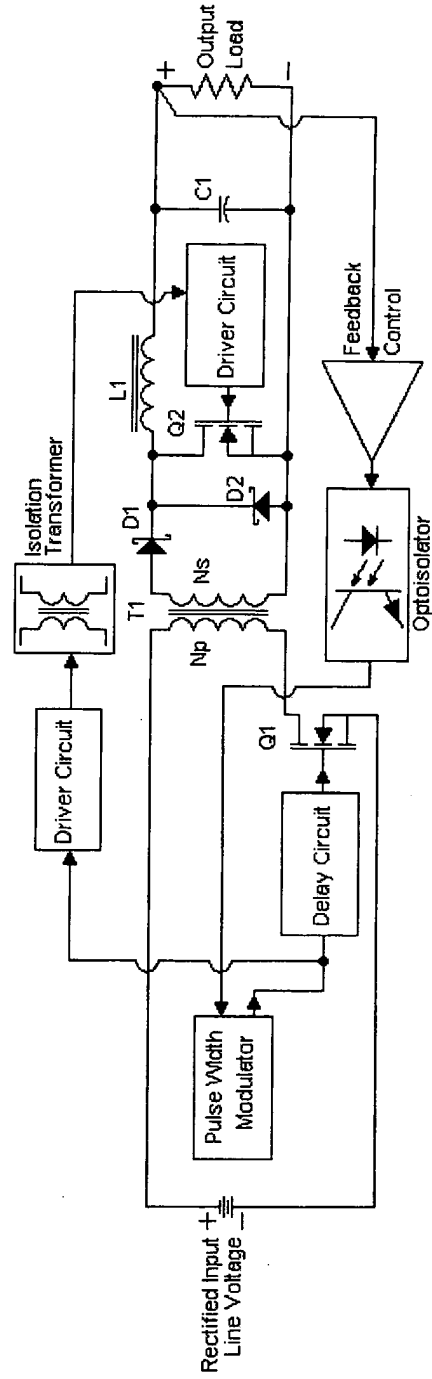


Fig. 1B (Prior Art)

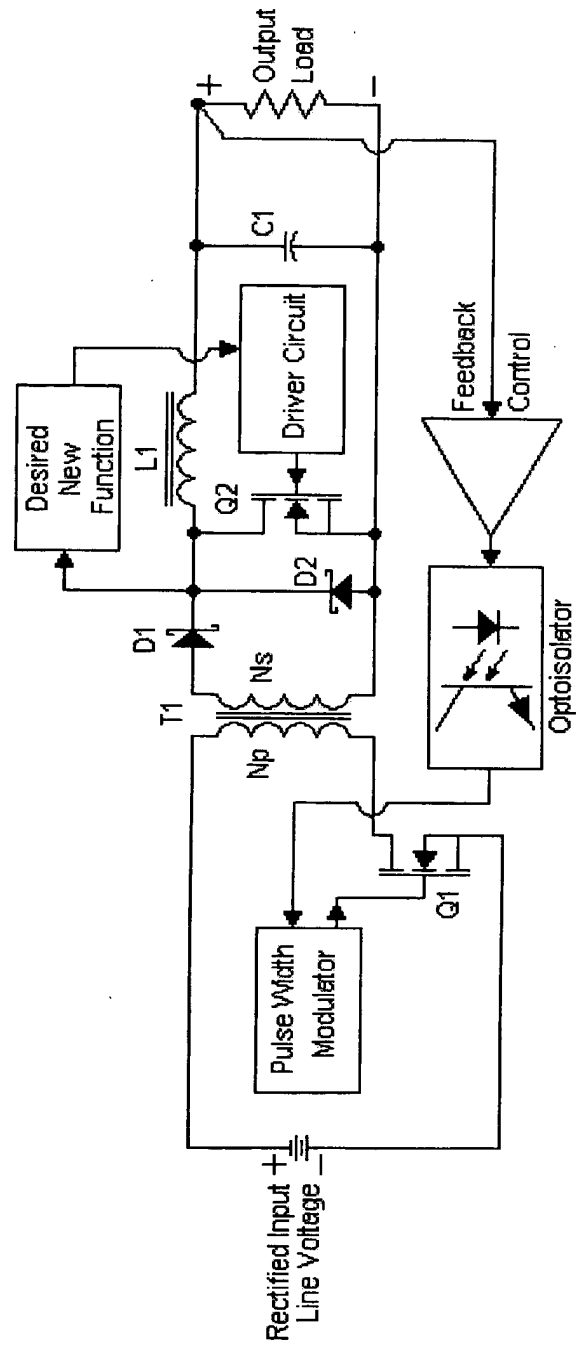


Fig. 2

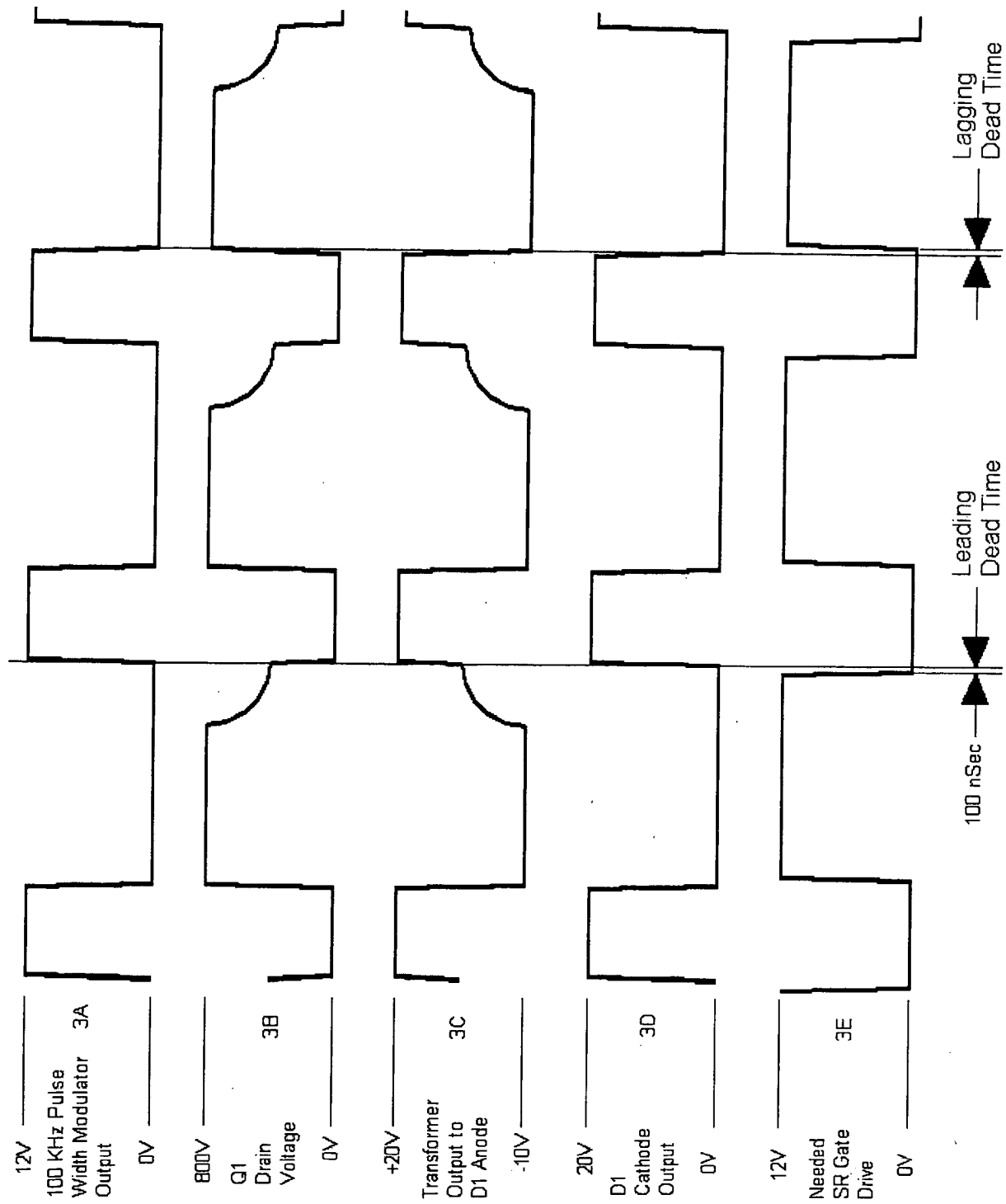


Fig. 3

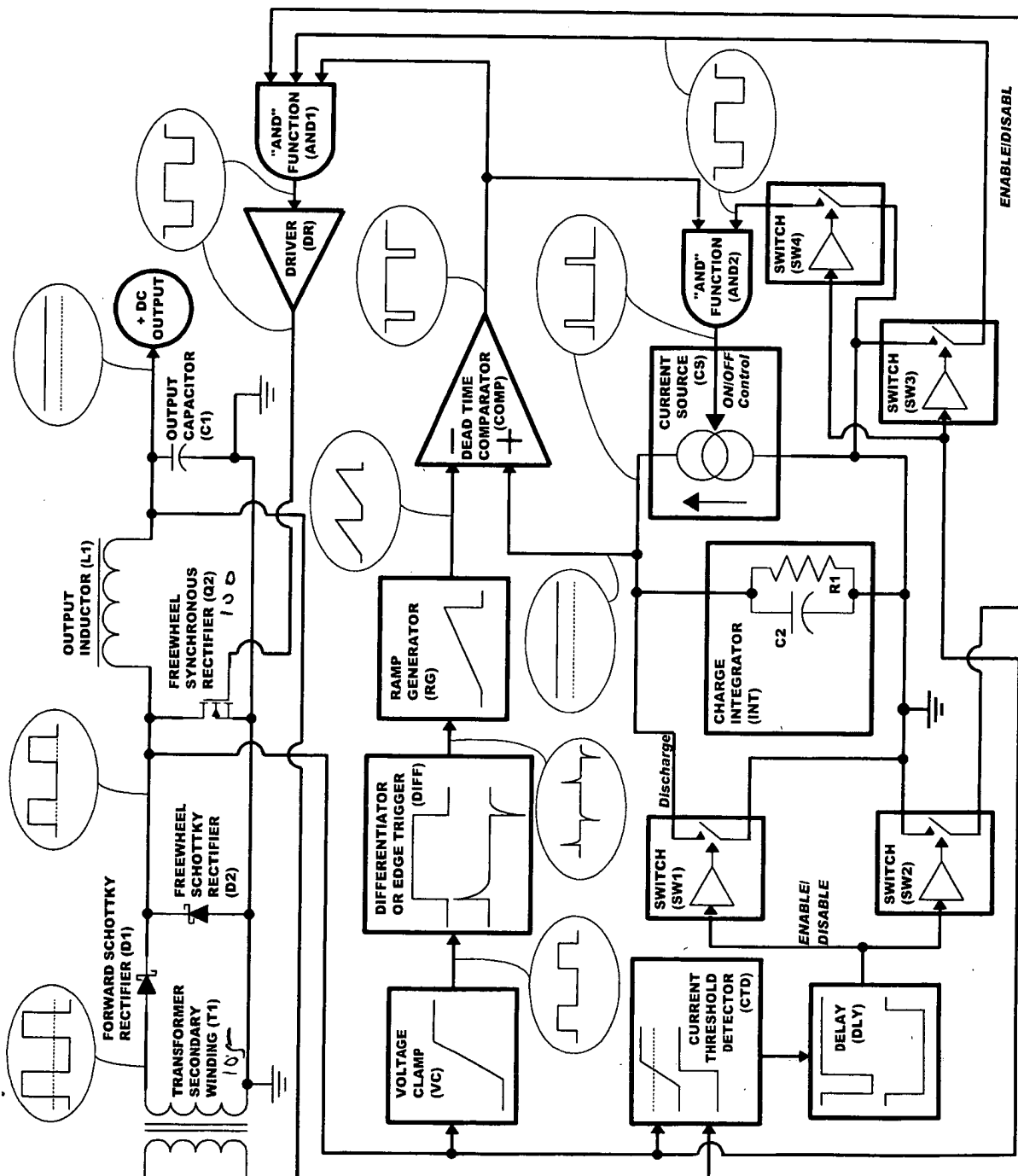


Fig. 4

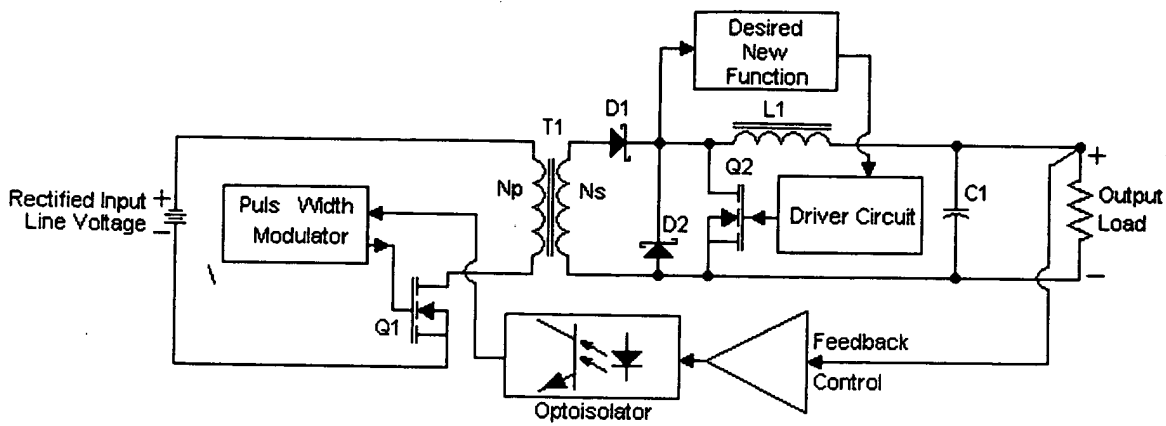


Fig. 4: Synchronous-Rectified Converter showing Desired New Control Function

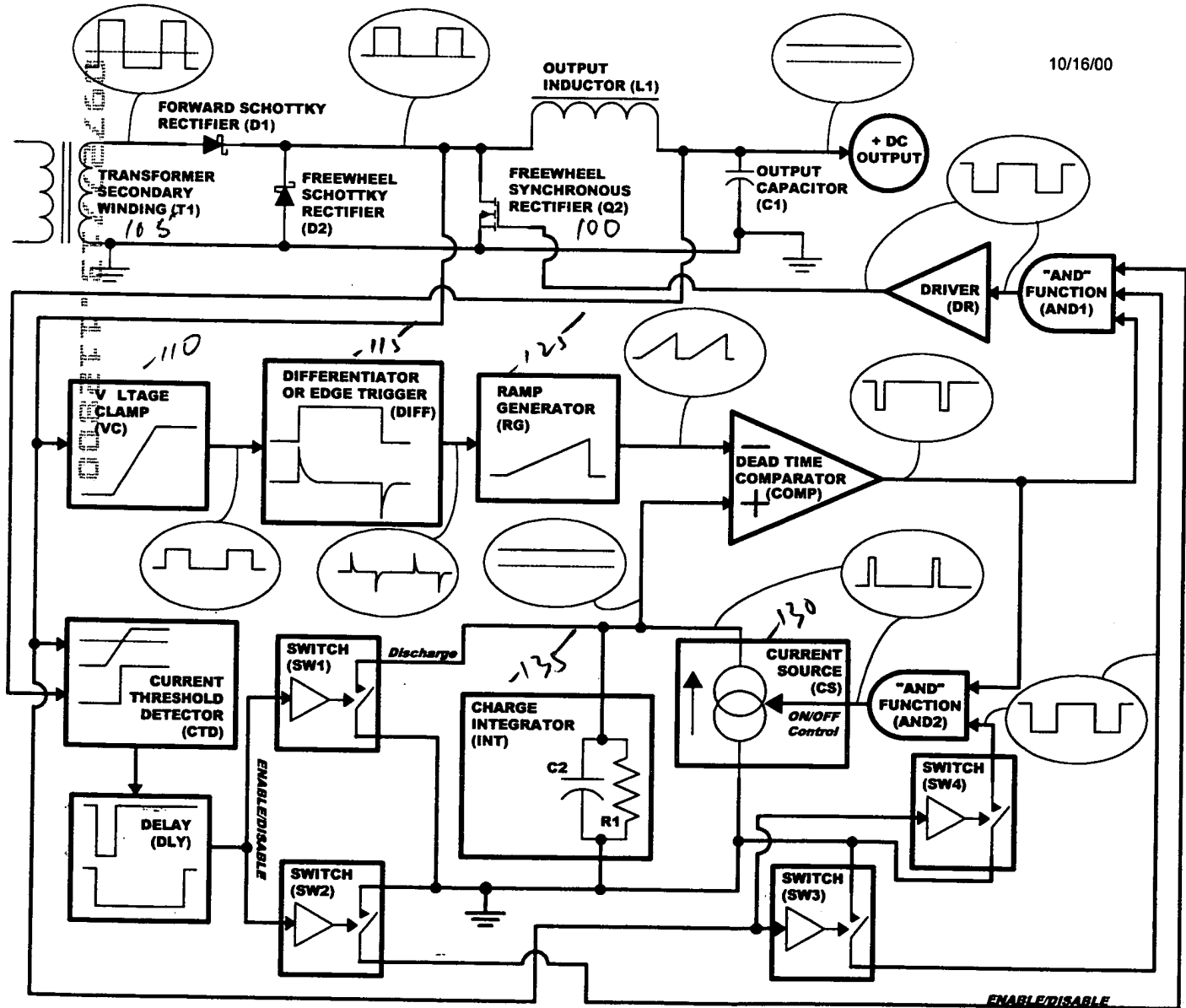


Figure 5: Block Diagram of Synchronous Rectifier System

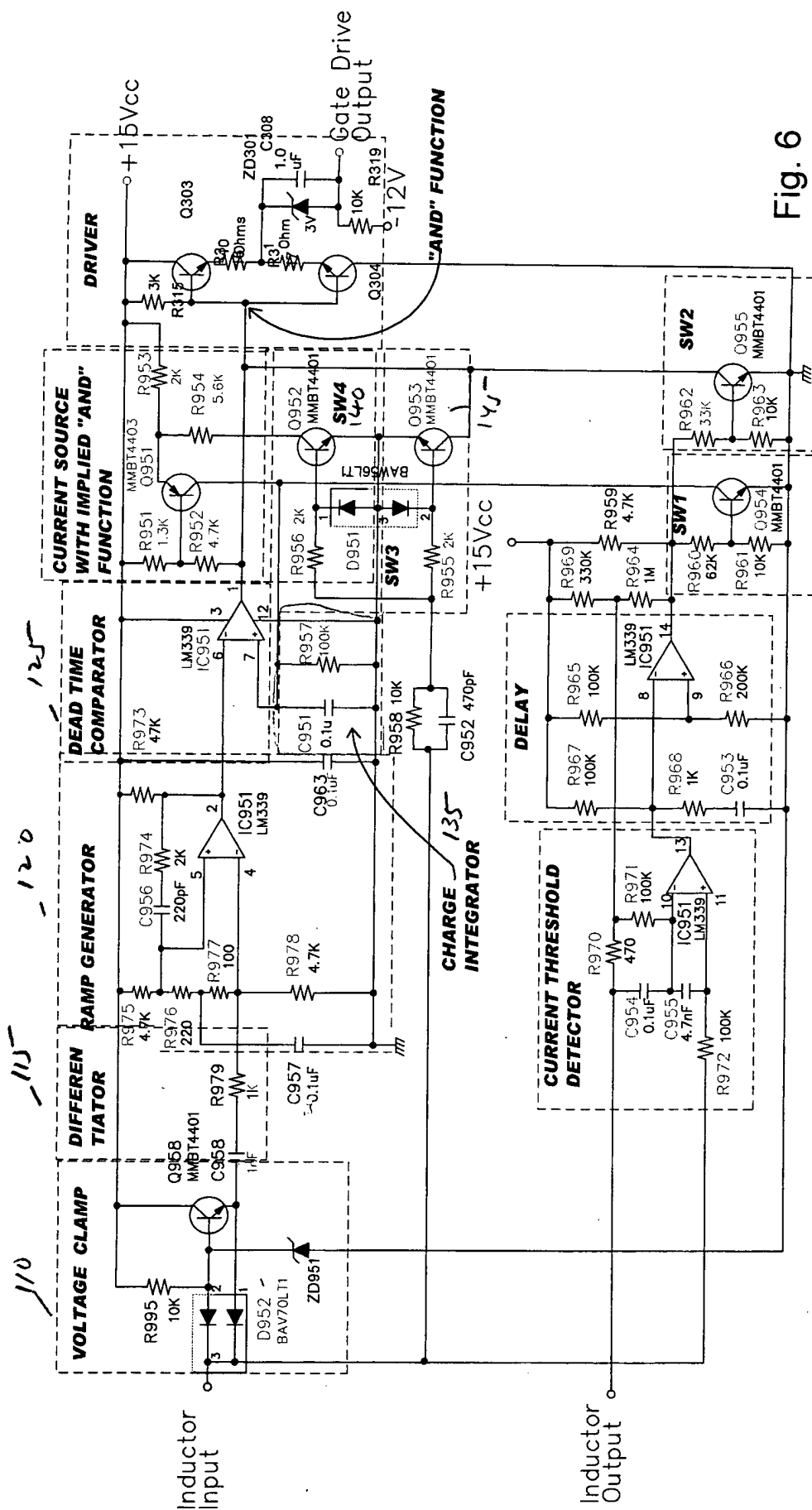
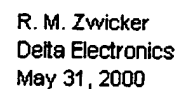


Fig. 6



I had intended to discard this old Fig. 6. Did you want to use it as 5A or 5B?

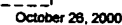


Fig. 7: Schematic of Synchronous Rectifier Controller

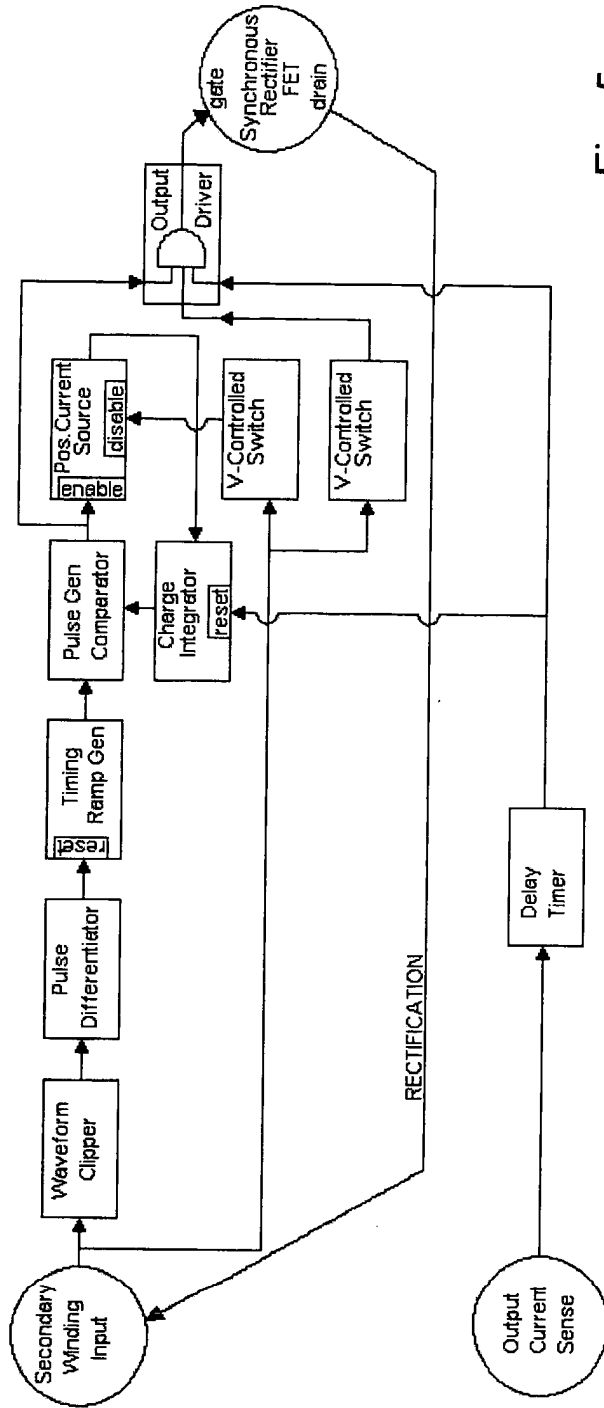


Fig. 5

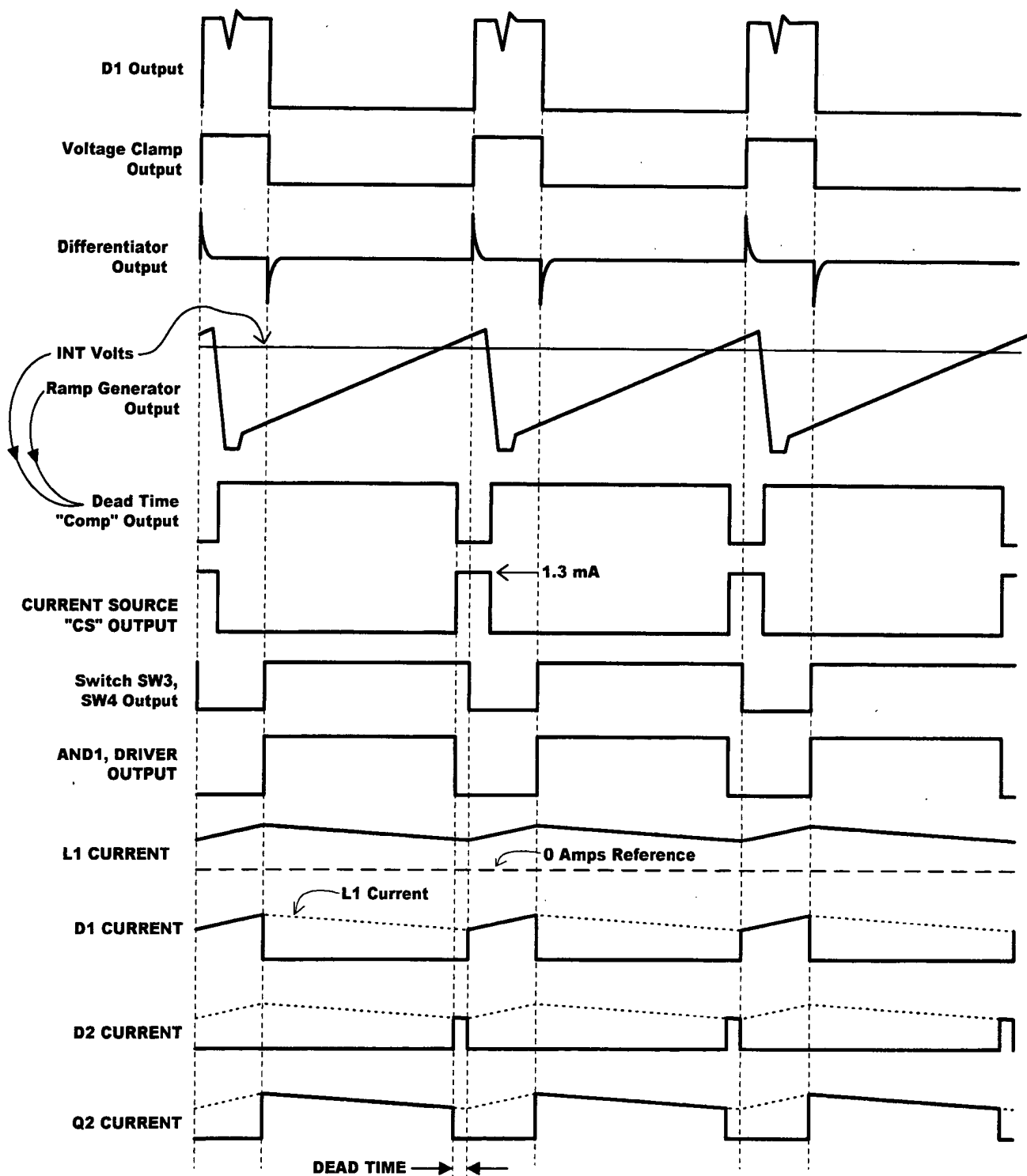


Fig. 7